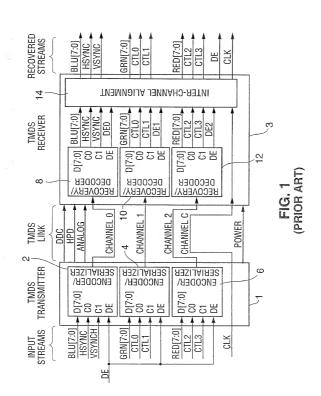
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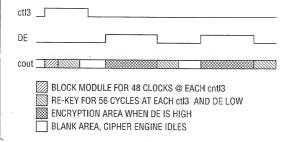
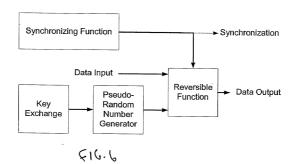


FIG. 2 (PRIOR ART)



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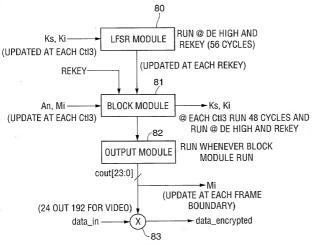
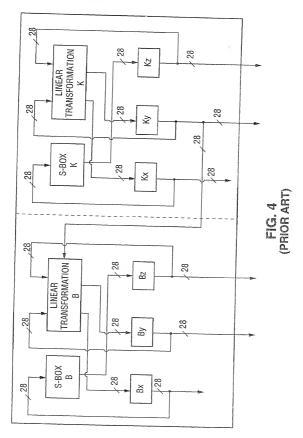


FIG. 3 (PRIOR ART)

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INPUT	В0	B1	В2	В3	В4	B5	В6	В7	K0	K1	K2	КЗ	K4	K5	K6	K7
ORIGIN	Bz	Bz	Bz	Bz	Bz	Bz	Bz	Ву	Kz	Kz	Kz	Kz	Kz	Kz	Kz	Ку
OUTPUT BIT																
0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23	17 5 22 19 19 3 8 9 26 1 20 14 19 3 6 3 25 23 11 9 22 27	26 20 5 3 6 7 21 5 13 0 13 15 7 23 6 0 5 4 21 11 26 17 12 24	22 15 14 15 17 4 27 7 23 19 9 5 18 1 21 27 14 2 27 14 2 27 14 27 27 23 19 20 20 20 20 20 20 20 20 20 20 20 20 20	27 24 16 11 18 8 2 4 10 11 14 3 12 12 25 23 22 6 9 20 17 4 10 8	21 2 25 21 122 16 11 8 11 13 10 13 17 24 23 19 24 22 10 1 8 27 7	18 25 17 16 7 6 24 13 7 16 4 25 1 8 18 18 14 15 12 23 0 20 9	2 0 20 27 9 5 12 3 15 24 1 16 16 18 10 4 2 12 13 14 0 15 25 18	5 16 11 1 12 17 3 15 19 18 2 27 0 9 8 7 21 26 22 4 24 6 13 23	3 20 7 6 25 27 17 9 13 0 14 6 11 8 19 16 3 11 1 21 20 18 13 20 18 21 21 21 21 21 21 21 21 21 21 21 21 21	6 18 19 14 6 14 26 10 12 5 23 21 22 4 0 21 5 14 16 11 21 12 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0 7 2 9 5 2 4 19 18 20 27 17 20 3 18 24 8 23 14 10 9 21 3 13	9 23 10 8 2 4 16 11 24 25 25 15 0 14 2 25 25 17 11 20 25 27 16 5 5 7 16 7 16 7 16 7 16 7 17 17 17 17 17 17 17 17 17 17 17 17 1	4 15 22 17 10 24 27 7 15 1 17 26 20 13 12 7 22 18 12 14	22 5 4 18 15 19 7 6 23 24 19 11 23 26 8 27 27 13 6 26 3 16 11 18	5 3 13 12 21 1 22 8 7 9 1 16 17 23 24 19 10 9 15 24 26 10	10 25 21 24 8 12 11 23 16 27 7 22 15 14 18 26 4 19 13 0 20 9 3

FIG. 5 (PRIOR ART)

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